

REMARKS

Reconsideration and allowance of the subject patent application are respectfully requested.

For the Examiner's convenient reference, this amendment is accompanied by a revised set of formal drawings including all of the changes noted herein; the changes made by the Request for Approval of Drawing Changes submitted on September 27, 2002 (approved in the office action of March 27, 2002); and the changes made by the Request for Approval of Drawing Changes submitted on June 17, 2003 (approved in the office action of September 2, 2003).

The drawings were objected to as failing to comply with 37 C.F.R. 1.84(p)(5). In response to this objection, Applicants have (1) designated the box labeled "Rasterize" in Figure 5 with the numeral "400b" as described, for example, at page 17, lines 6-9; and (2) identified the DATA lines in Figure 17 as described, for example, at page 45, lines 4-14. Based on these changes, withdrawal of the objection under 37 C.F.R. 1.84(p)(5) is respectfully requested.

The drawings were objected to as failing to comply with 37 C.F.R. 1.84(p)(4) because the character "130" has been used to designate both the parallel bus in Figure 2 and the Graphics Memory Request Arbitration in Figure 4. In response to this objection, Figure 4 has been changed to designate the Graphics Memory Request Arbitration with the numeral 131. A corresponding change has been made to page 16 of the specification. Based on these changes, withdrawal of the objection under 37 C.F.R. 1.84(p)(4) is respectfully requested.

An additional change has been made to Figure 3 to designate the Controllers with the numeral 52 (rather than 108) for consistency with the specification and the other drawings.

The disclosure was objected to because of informalities noted on pages 2-3 of the office action. In response to these objections, Applicants have:

- corrected the replacement paragraph on page 3 of paper no. 8 (corresponding to the paragraph beginning on page 14, line 8 of the specification) to refer to an audio codec, rather than audio code;
- corrected page 15, line 25 to refer to main memory 112, rather than main memory 110;
- changed the reference to transform unit 300b at page 17, line 2 to transform unit 300 for consistency with the other references to transform unit in the specification (e.g., page 16, line 20 *et seq.*);
- changed the brief description of Figure 6 on page 8 to refer to an "example input/output subsystem" for consistency with the drawing changes to Figure 6;
- amended the sentence beginning at page 34, (numbered) line 8 to read "Figure 8 is an even more detailed overall view of serial interface 1000." to better describe Figure 8;
- changed the reference to display controller 162 at page 39, line 26 to display controller 164;
- changed the reference to serial interface 100 at page 40, line 6 to serial interface 1000;
- changed the references in the specification to communication RAM 1410 to communication RAM 1014 and made a corresponding change to Figure 11; and
- changed the reference to input/output buffers 1414 at page 41, line 7 to input/output buffers 1016 and made a corresponding change to Figure 11.

Entry of the above-described amendments to the specification and approval of the above-described changes to the drawings are respectfully requested. Based on these changes to the specification and drawings, withdrawal of the objection to the disclosure is respectfully requested.

Claim 30 was objected to based on certain language in the last line of the claim. Applicant has adopted the change kindly suggested by the Examiner and respectfully requests withdrawal of the objection to claim 30.

Claims 2-12, 14-18 and 21-47 were rejected under 35 U.S.C. Section 112, first paragraph, as allegedly including subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. The office action states that the claimed invention is described on pages 18-38 and shown in Figures 6-12 but that it is unclear how the claimed invention ties into the rest of the disclosure, particularly pages 1-17 and Figures 1-5. The office action specifically notes that "it is not clear how the Input/Output Subsystem shown in figure 6 is tied into the system as a whole since the Input/Output Subsystem is not introduced earlier in the specification nor shown anywhere in any of figures 1-5."

Applicants respectfully submit that the specification as filed is fully and completely enabling for one of ordinary skill in the art to make and/or use the claimed invention. Indeed, the office action does not identify or allege any defect in the specification that would prevent the making and using of the claimed invention. Rather the office action questions how Figure 6 relates to the elements shown in Figures 1-5.

Some confusion may have resulted from the changes made to Figure 6 and the specification in the response to the first office action. As filed, Figure 6 and the specification referred to peripheral controller 162 as including a serial interface 1000, an external interface 1100, a disk interface 1200 and an audio interface 1300. However, Figure 3 clearly shows that the audio interface (originally identified as 160) is separate from the peripheral controller 162. It is apparent from reviewing Figures 2, 3 and 6 that the example peripheral controller 162 includes example serial interface 1000 which relates to the inputs/outputs from controllers 52; example external interface 1100 which relates to the inputs/outputs over buses 132, 142 to which PROM 134, modem 136 and flash memory 140 may be connected; and example disk interface 1200 which relates to the inputs/outputs over bus 130 from mass storage access device 106. In the response to the first office action, Figure 6 and the relevant portion of the specification were amended

so that the reference numeral "162" did not refer to the elements inside the dotted-line box (*i.e.*, elements 1000, 1100, 1200, and 1300) as the peripheral controller since element 1300 is not part of the peripheral controller. By removing the reference numeral 162, these elements (*i.e.*, the serial interface 1000, the external interface 1100, the disk interface 1200 and the audio interface 1300) were designated by the "Input/Output Subsystem" legend present in the originally-filed Figure 6. This legend accurately describes the elements in the dotted-line box as a subsystem for inputting/outputting data to/from the video game system.

It is quite clear that Figure 6 "fits together" with the earlier Figures. For example, Figure 6 specifically illustrates that the line from hand controller(s) 52 to graphics and audio processor 114 in Figure 2 (also shown in Figure 5 as the line into peripheral controller 162 from the legend "Controllers 52") includes the signal lines SID 1[3:0] and SID 0[3:0]. Figure 6 also specifically identifies that the buses 132, 142 shown in Figure 2 (to which the PROM 134, modem 136, flash memory 140 and memory card 144 shown in both Figures 2 and 3 may be connected) include the signal lines EXI0DO0, EXI0DI0, etc. Figure 6 further specifically identifies that the bus 130 to which the mass access storage device 106 is connected (also shown as the line from the legend "Mass Storage Access Device 106" to peripheral controller 162 in Figure 3) includes the signal lines DICOVER, DIERRb, etc. Finally, Figure 6 specifically identifies that the lines from mass storage access device 106 to audio interface 1300 (*e.g.*, for streaming audio) (*see* Figure 3) and the lines from audio codec 122 to audio interface 1300 (*see* Figure 3) include the signal lines AID0, AILRO, etc.

In summary, Applicants respectfully submit that the specification is fully and completely enabling for the claimed subject matter. In addition, Applicants respectfully submit that Figure 6 clearly "fits together" with the earlier Figures. For these reasons, Applicants respectfully request that the Section 112, first paragraph, rejection of claims 2-12, 14-18 and 21-47 be withdrawn.

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Because the claims are not otherwise rejected, claims 2-12, 14-18 and 21-47 are believed to be in condition for allowance and early notice to that effect is respectfully requested.

Respectfully submitted,

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By:



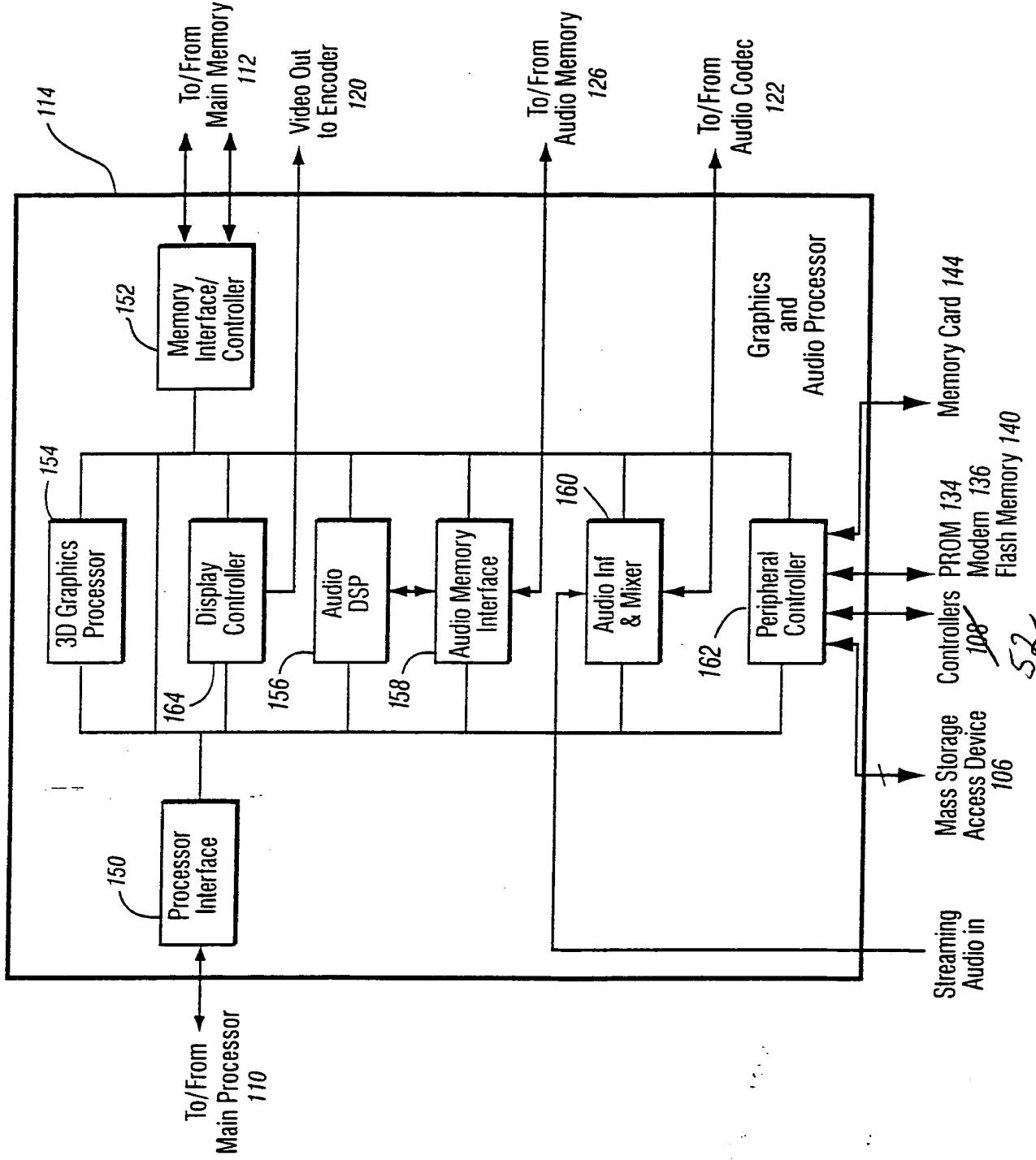
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Fig. 3



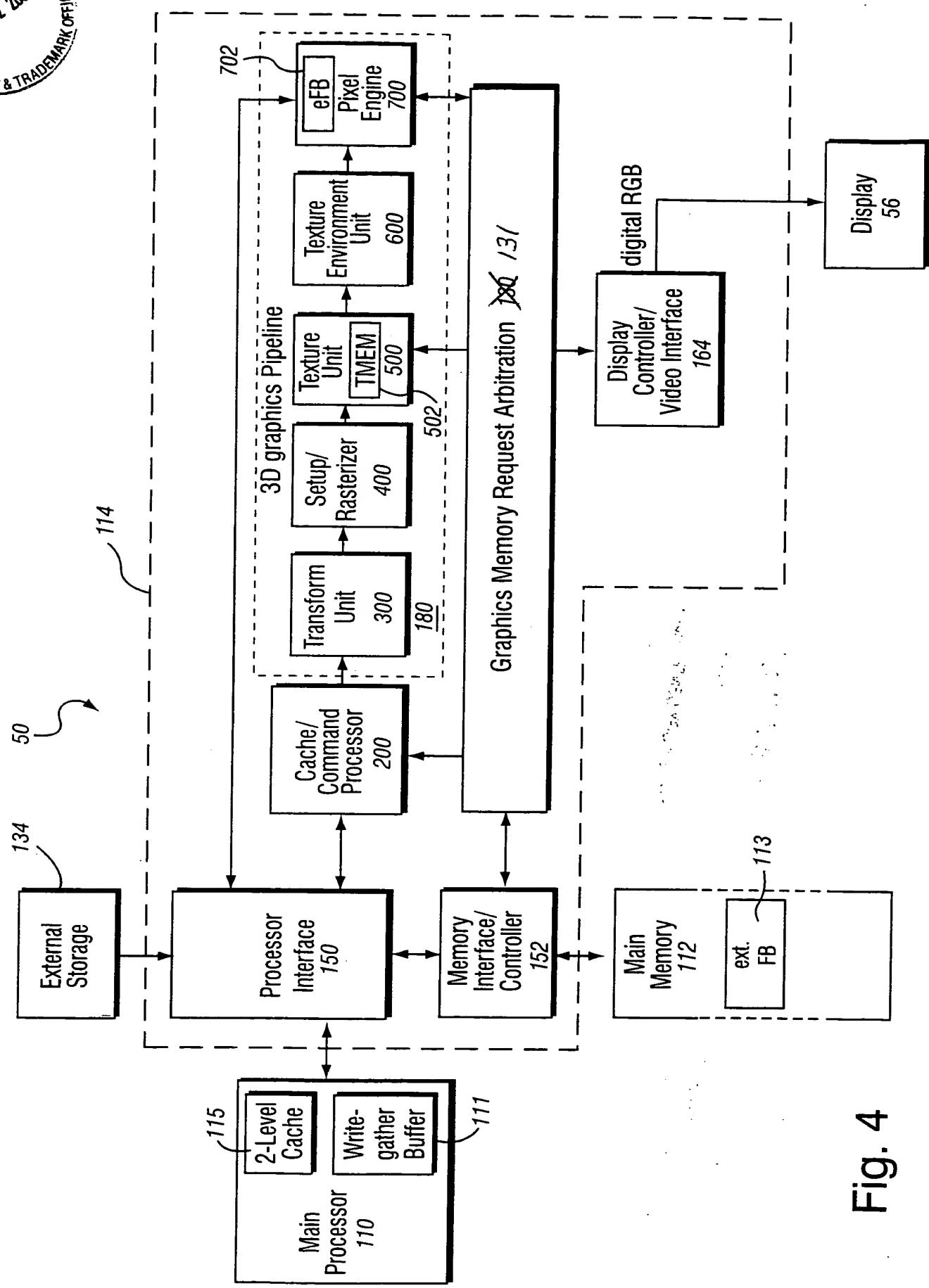


Fig. 4

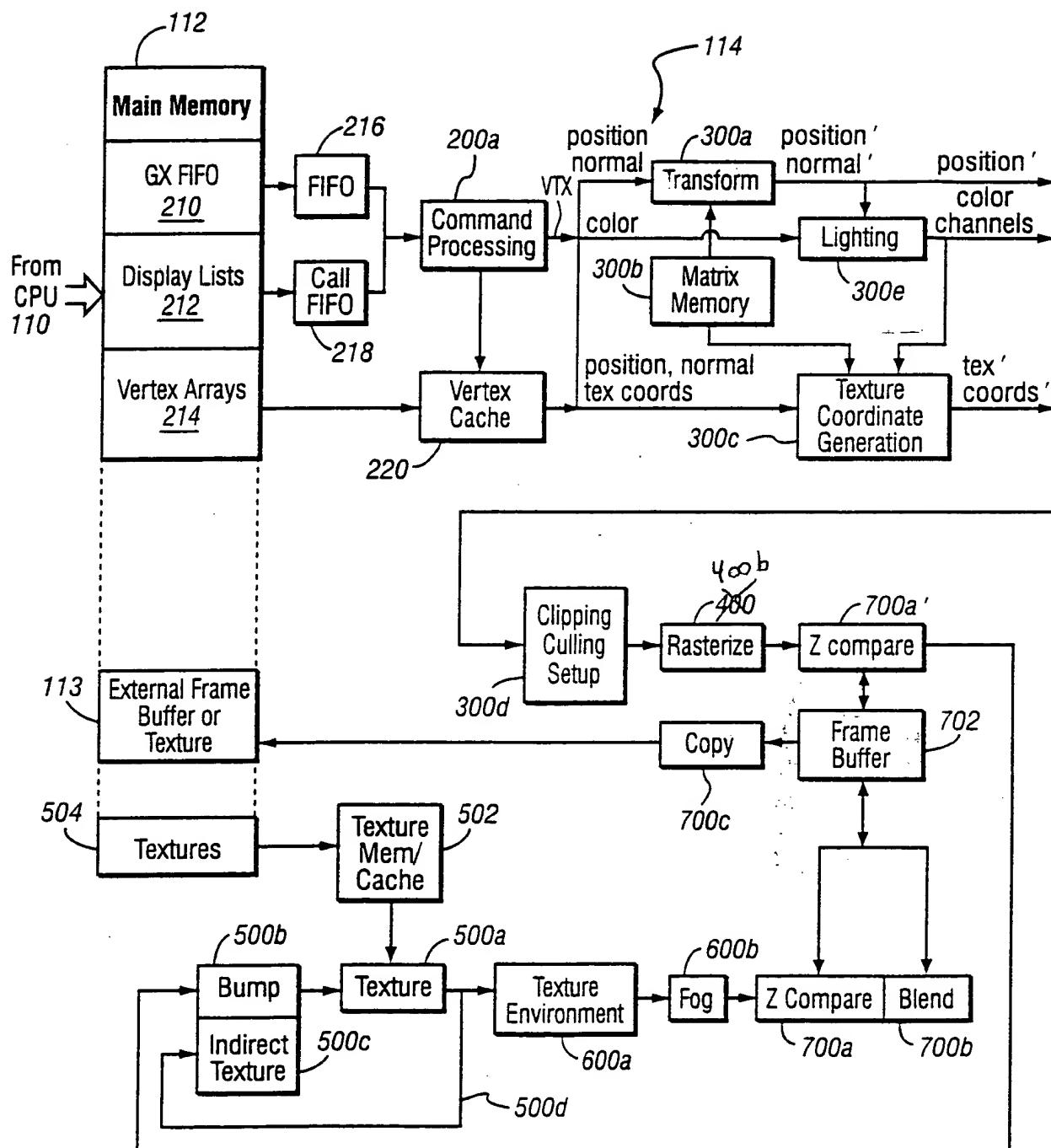


Fig. 5 EXAMPLE GRAPHICS PROCESSOR FLOW

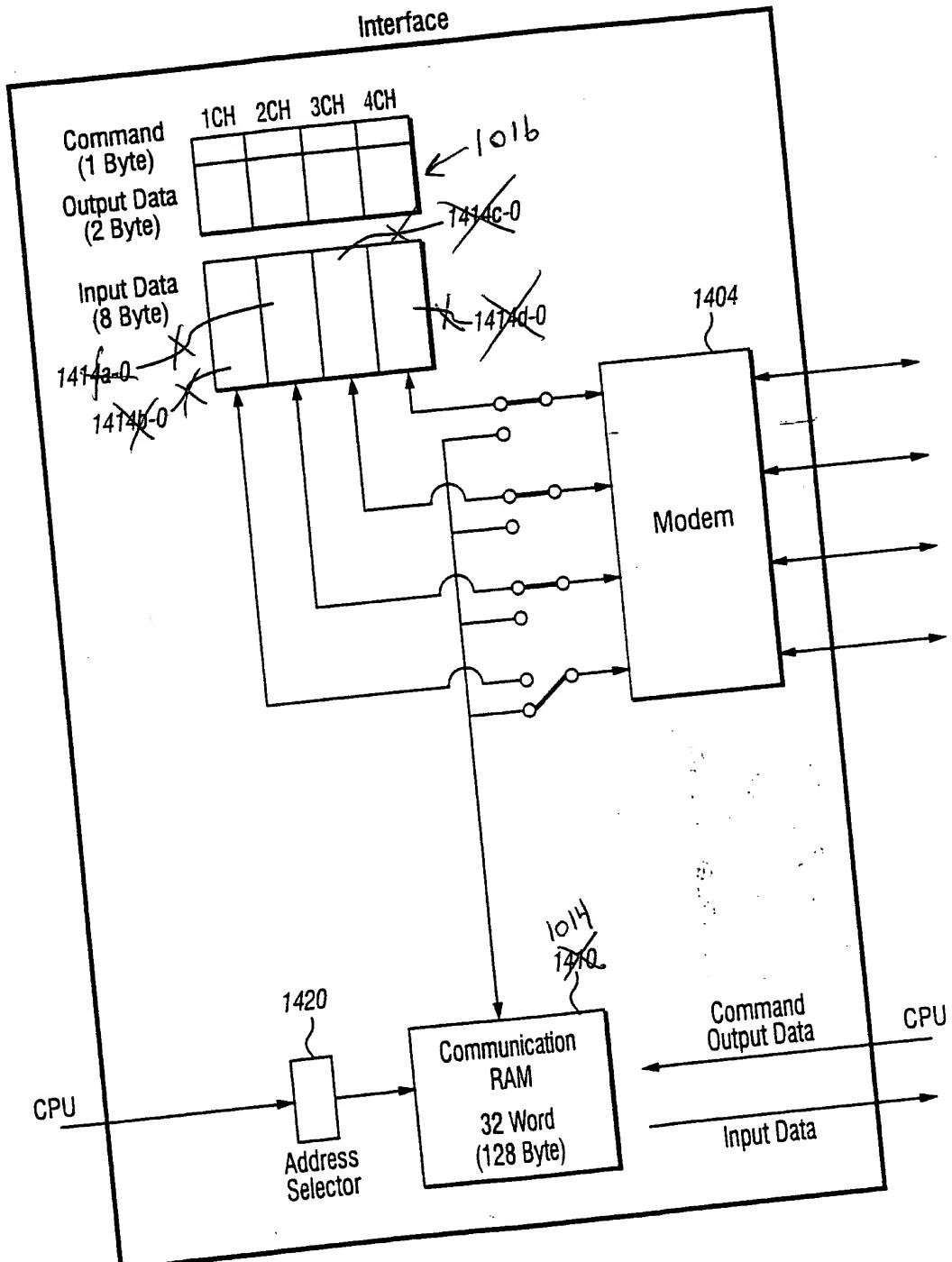
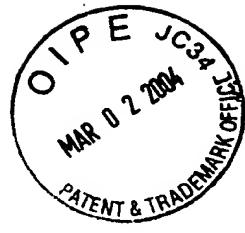


Fig. 11



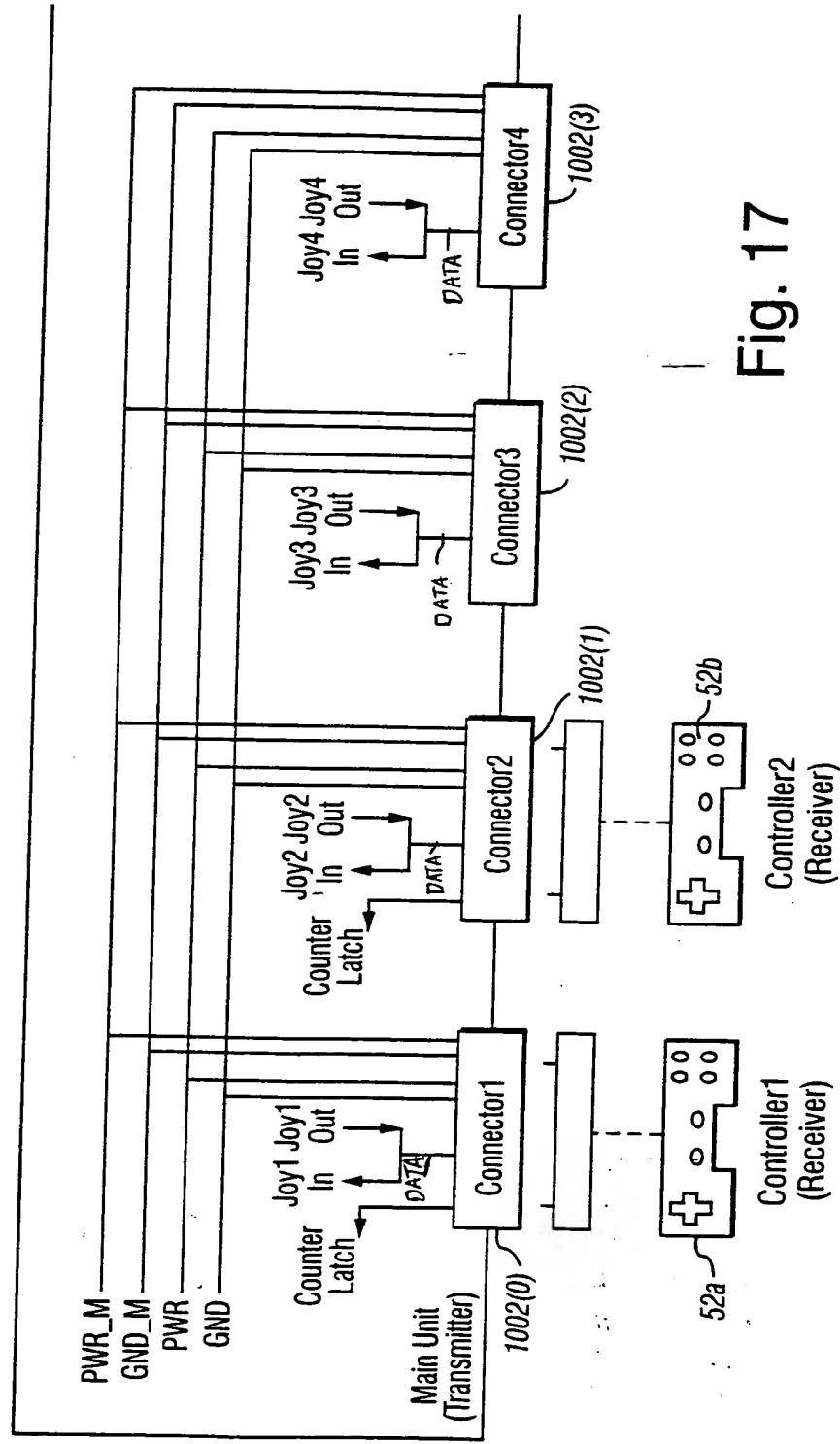
**Appl. No. 09/722,664
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ANNOTATED SHEET
SHOWING CHANGES**

Diagram illustrating the flow of data through a signal processing system:

- Input: S enters the **Separation** block.
- Output: The **Separation** block outputs **Data**.
- Output: The **Data** signal enters the **End** block.
- Output: The **End** block outputs the **Signal**.

Fig. 16A

Fig. 16B Response Frame



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